

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,023,057 B2
APPLICATION NO. : 10/799380
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INVENTOR(S) : Meikei Ieong et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 4, Line 45:

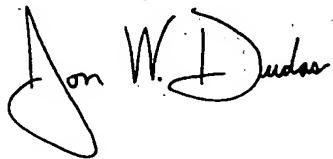
"NFET" should read -- nFET --

Column 12, Line 22:

"FIG. 1" should read -- FIG. 11 --

Signed and Sealed this

Seventh Day of November, 2006



JON W. DUDAS
Director of the United States Patent and Trademark Office